



## AGENDA – HIPEAC conference Workshop

22nd of January 2018, Manchester, UK

Meeting venue: Room 8 of the Manchester Central Convention Center.

SAFURE time slot: 10:00 – 17:30			
From	To	Topic	Speaker
10:00 – 10:30		<b>Official Welcome and short introduction of SAFURE, short project presentation</b>	André Osterhues (ESCR)
10:30 – 11:00		<b>Safety and security – towards a combined approach for mixed-critical cyber-physical systems</b> (incl. Q&A)	André Osterhues (ESCR)
11:00 – 11:30		Short coffee break	
11:30 – 13:00		<b>Session 1 [Chair Edin Arnautovic (TTT)]</b>	
11:30 - 12:00		<b>Modeling and analysis of Mixed-Critical systems</b> (incl. Q&A)	Marco Di Natale (SSSA)
12:00 - 12:30		<b>Security Risks posed by Temperature Measurements in Mobile Platforms</b> (incl. Q&A)	Philipp Miedl (ETHZ)
12:30 – 13:00		<b>Secure Update for the Mixed Critical Systems</b> (incl. Q&A)	Sergey Tverdyshev (SYSGO)
13:00 – 14:00		Lunch	
14:00 – 15:30		<b>Session 2 [Chair Borislav Nikolic (TUBS)]</b>	
14:00 - 14:30		<b>Worst-Case Timing Analyses of Ethernet in SymTA/S</b> (incl. Q&A)	Björn Gebhardt (SYM)
14:30 - 14:50		<b>Real-time Ethernet in Automotive Systems</b> (incl. Q&A)	Robin Hofmann (TUBS)
14:50 - 15:10		<b>Deterministic Ethernet Security</b> (incl. Q&A)	Edin Arnautovic (TTT)
15:10 - 15:30		<b>Secure CAN communication</b> (incl. Q&A)	Stefania Botta (MAG)
15:30 – 16:00		Coffee break	
16:00-17:00		<b>Session 3 [Chair Jaime Abella (BSC)]</b>	
16:00 – 16:30		<b>A Practical Methodology to Tightly Upperbound Contention in COTS Multicores</b> (incl. Q&A)	Jaime Abella (BSC)
16:30 - 17:00		<b>METriCS: a Measurement Environment for Multi-Core Time Critical Systems</b> (incl. Q&A)	Sylvain Girbal (TRT)
17:00 – 17:30		<b>Conclusions and future steps [Chair André Osterhues (ESCR)]</b>	